Title: ADAPTIVE EXTENSION OF SPECULATIVE....

Inventor(s): Travis D. Fox et al.

Dkt. No.: STL11083

Filed: June 23, 2003

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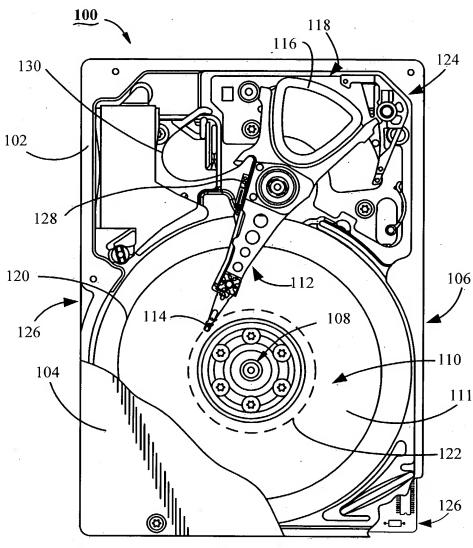


FIG. 1

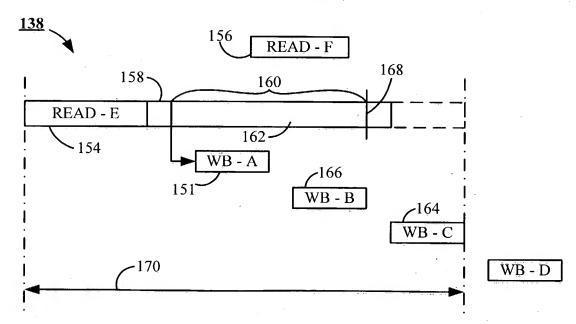


FIG. 3

Dkt. No.: STL11083 Filed: June 23, 2003 <u>2/5</u> 120 118 DISC 110~ READ/ WRITE HEAD VCM 146 152 MOTOR DRIVER PREAMP 132 148 READ/WRITE CHANNEL DEMOD SERVO ENGINE DAC 142 144 RLA EXTENSION CONTROLLER ROUTINE CHANNEL OF 134 INTERFACE THE DRIVE HOST 100 136 138 150 PROCESSOR IC MEMORY CACHE INTERFACE CHANNEL OF THE DRIVE HOST

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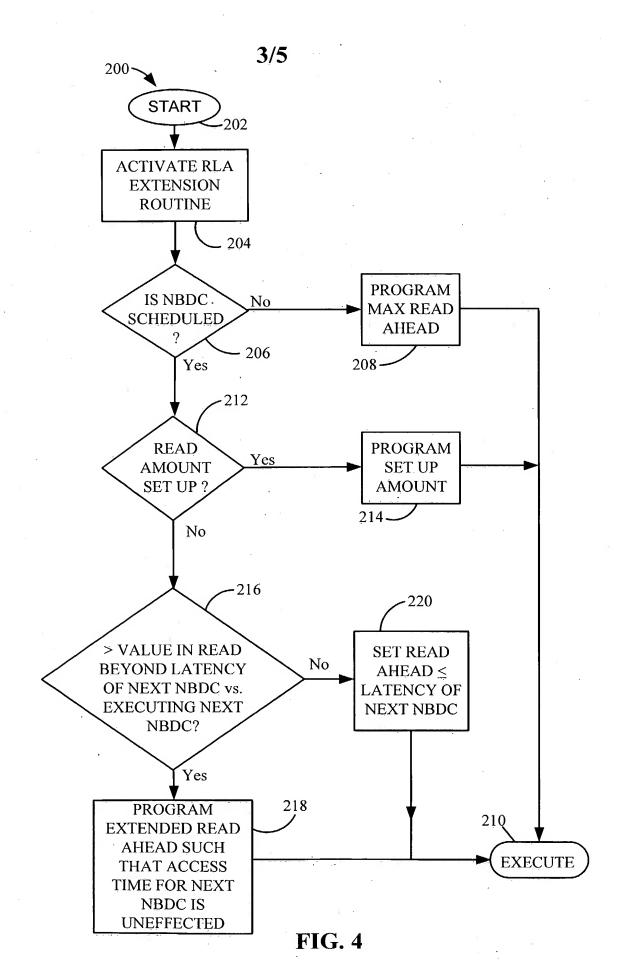
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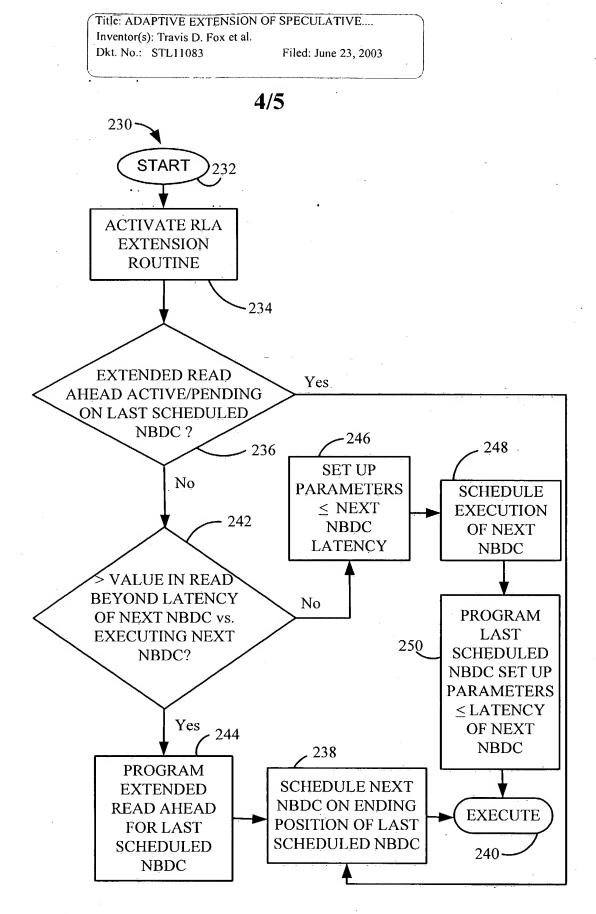


FIG. 5

Dkt. No.: STL11083 Filed: June 23, 2003 5/5 138 320 260 322 **READ - E** WB - A START WB - B -262 WB - C -324 WB - D ACŢIVATE RLA **EXTENSION READ - F ROUTINE** READ - G 322 READ - H -264 WB - QQ **SCHEDULE** 324 WB - QR **NBDC FOR EXECUTION** WB - QS **READ - MF** -266 **FIG.** 7 268 EXTENDED READ No AHEAD ACTIVE/PENDING ON JUST PRIOR SCHEDULED **READ NBDC?** 274 -Yes SET & 272 **PROGRAM** > VALUE **JUST PRIOR** IN READ **SCHEDULED NBDC READ BEYOND LATENCY OF** No AHEAD TO JUST SCHEDULED NBDC vs. EXECUTING JUST LATENCY OF **SCHEDULED JUST SCHEDULED** NBDC? **NBDC** Yes 276 **PROGRAM** 270 -**EXTENDED READ** AHEAD SUCH **EXECUTE** THAT ACCESS TIME FOR NEXT **NBDC IS** FIG. 6 UNEFFECTED

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